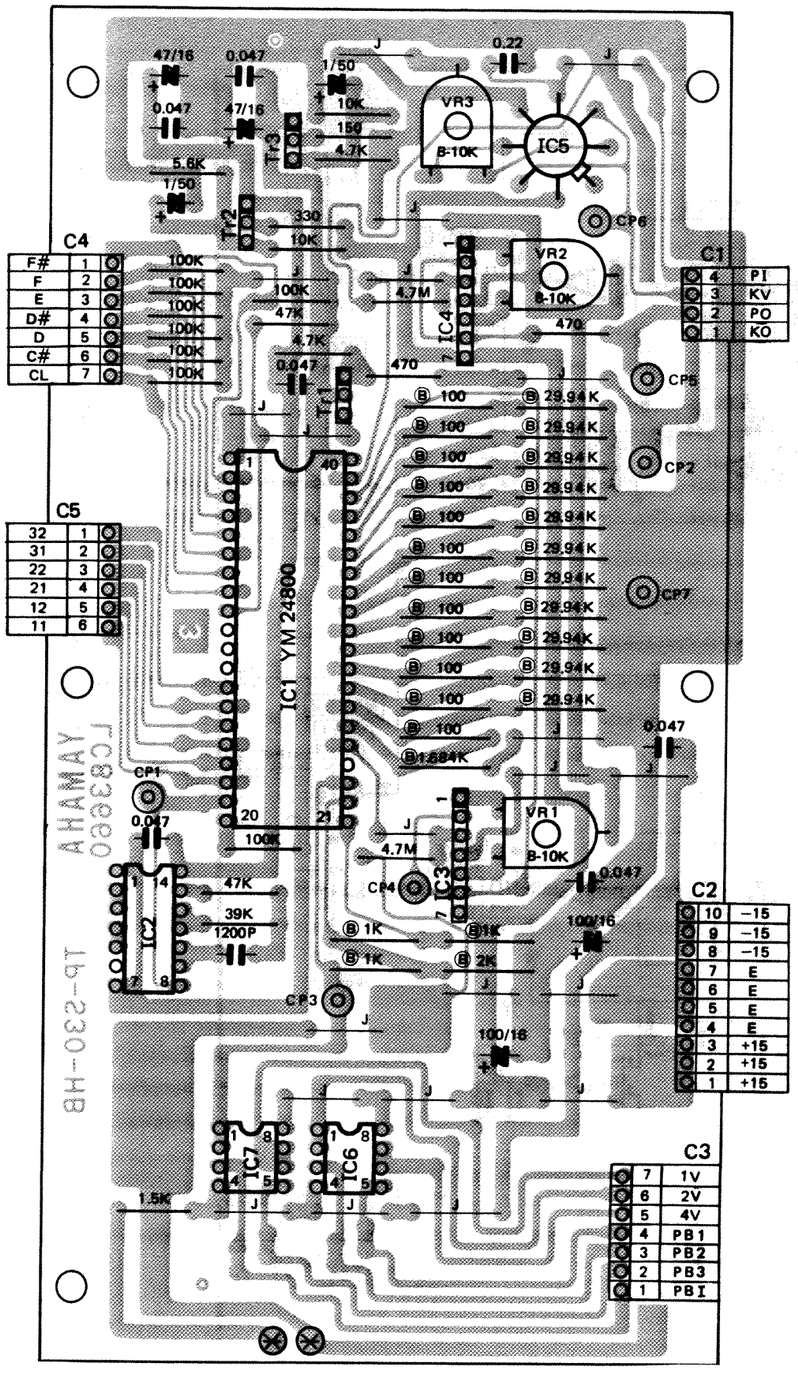
SSK Circuit Board



Note)

1. Printed Circuit Board' LC83660 [3]
2. IC
   - IC 1 : YM24800
   - IC 2 : TC4069UBP
   - IC 3, 4 : TA7504S
   - IC 5 : TA7505M
   - IC 6, 7 : NJM4558DN
3. Transistors
   - Tr 1, 3 : 2SA1015 (Y)
   - Tr 2 : 2SC1815 (Y)
4. Resistors
   - Marked Ⓑ : 0.1% metal film
5. Capacitors
   - No mark : Ceramic
   - Marked (マ) : Mylar

● YM24800

Terminal No.	Name	Description
①	Vss	+7.5V terminal (+5V in CS-5 and CS-15)
②	K7	F#, C Note ON Data INPUT
③	K6	F, B 〃
④	K5	E, A# 〃
⑤	K4	D#, A 〃
⑥	K3	D, G# 〃
⑦	K2	C#, G 〃
⑧	K1	Lowest C (CL) key input terminal
⑨	K0	Key on trigger
⑩	KOD	Key on data (unused in CS-5 and CS-15)
⑪	K42	37Keys — 44keys $G_5 \sim C_6^\#$ (Unused in 37key model)
⑫	K41	— $C_6^\# \sim F_5^\#$ (Unused in 37key model)
⑬	K32	$G_5 \sim C_6$ $G_4 \sim C_5$ 1/2 Octave ON Data INPUT
⑭	K31	$C_5^\# \sim F_5^\#$ $C_4^\# \sim F_4^\#$ 〃
⑮	K22	$G_4 \sim C_5$ $G_3 \sim C_4$ 〃
⑯	K21	$C_4^\# \sim F_4^\#$ $C_3^\# \sim F_3^\#$ 〃
⑰	K12	$C_3 \sim C_4$ $G_2 \sim C_3$ 〃
⑱	K11	$C_3 + C_3^\# \sim F_3^\#$ $F_2 \sim F_2^\#$ 〃
⑲	∅	Clock pulse input terminal. Drives state counter.
⑳	IC	Initial clear. Initializes SSK when power switch is truned on.
㉑	01	Octave voltage input (lowest octave)
㉒	02	Octave voltage input
㉓	03	Octave voltage input
㉔	04	Octave voltage input (Highest octave) (Not provided in 37key model)
㉕	00	Octave voltage output
㉖	C1	C (1 Octave low) Key Voltage INPUT (Lder resistor voltage)
㉗	C#	C#-Key Voltage Input (ladder resistor voltage)
㉘	D	D-key 〃
㉙	D#	D#-key 〃
㉚	E	E-key 〃
㉛	F	F-key 〃
㉜	F#	F#-key 〃
㉝	G	G-key 〃
㉞	G#	G#-key 〃
㉟	A	A-key 〃
㊱	A#	A#-key 〃
㊲	B	B-key 〃
㊳	C	C-key 〃
㊴	NO	Keyboard voltage output
㊵	VDD	−7.5V terminal (−10V in CS-5 and CS-15)

SSK C/B
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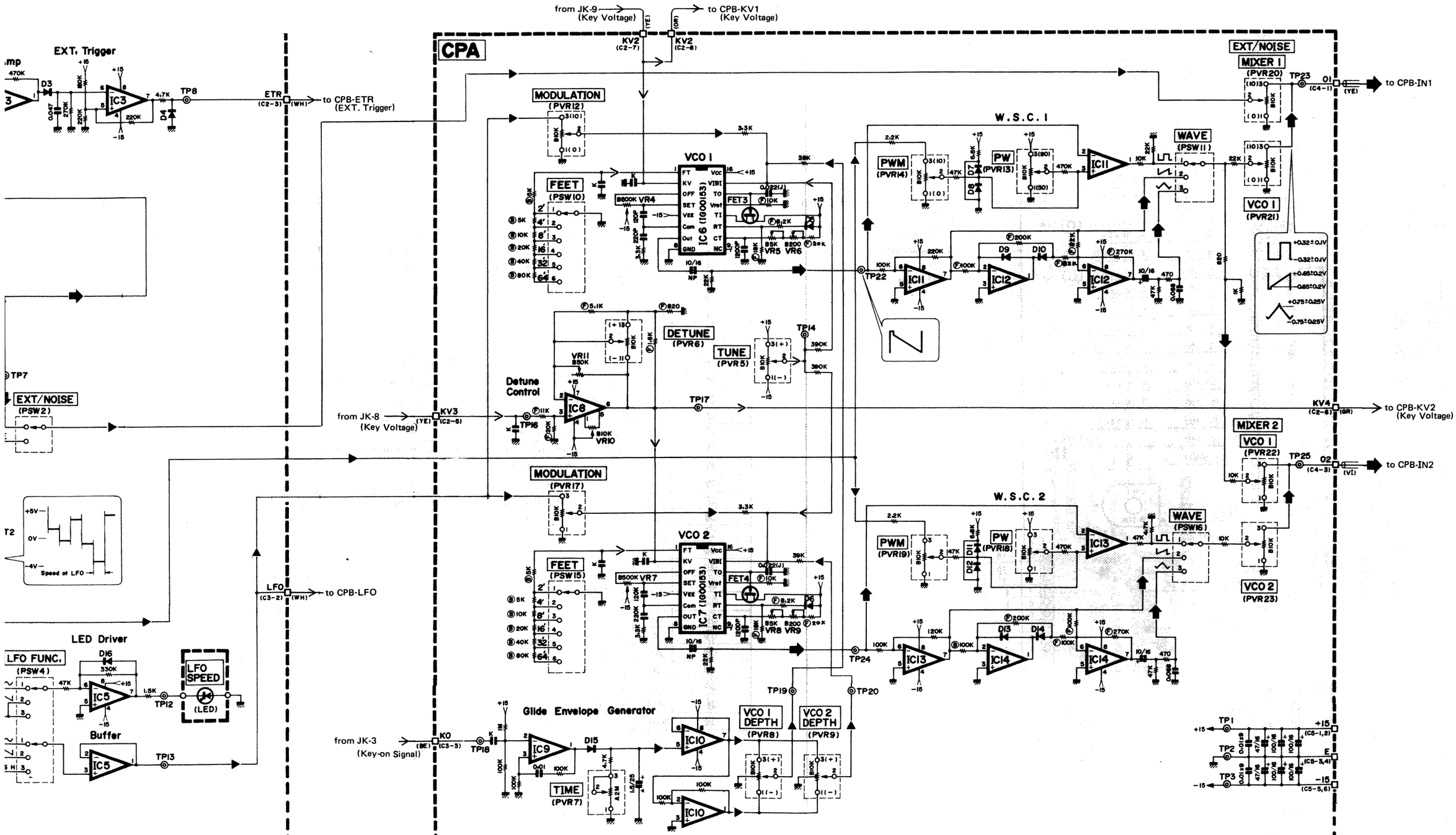
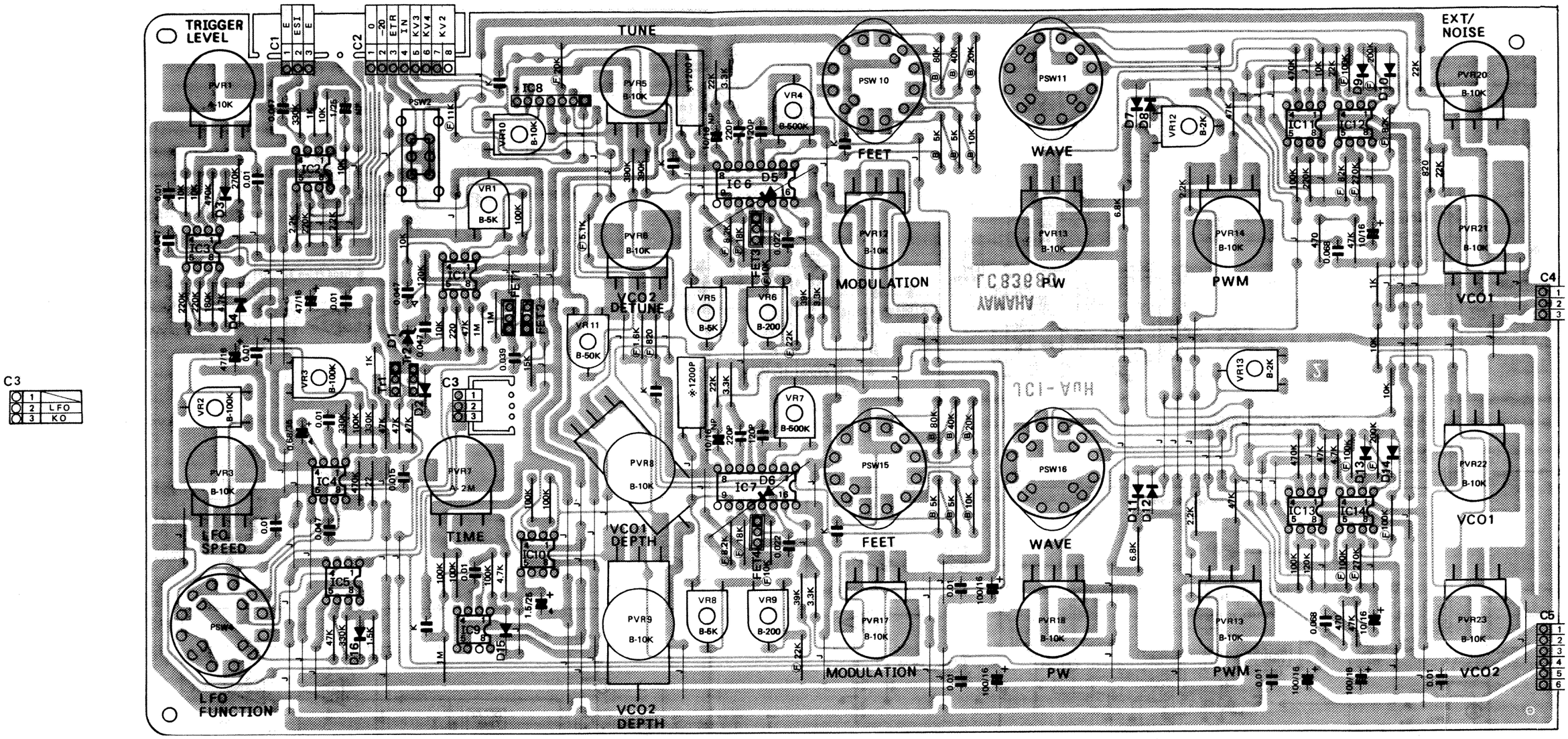
CPA Circuit Diagram





Note)

1. Printed Circuit Board LC83680 [2]
2. IC 1 ~ 3, 5, 9 ~ 14 : NJM4558DN
   IC 4 : IG00150
   IC 6,7 : IG00153
   IC 8 : TA7504S
3. Transistors
   Tr 1,2 : 2SA1015 (Y)
4. FET
   FET 1, 3, 4 : 2SK30A (Y)
   FET 2 : 2SK105 (E)
5. Diodes
   D 1 : 1S1715P
   D 2 ~ 16 : 1S1555
6. Resistors
   Marked Ⓑ : 0.1% metal film
   Marked Ⓕ : 1% metal film
7. Capacitors
   Marked ※ : Moistureproof polyethyrene
   Marked " マ " : Mylar
   Marked K : 1,000pF
   Marked △ : Tantalum
   No mark : Ceramic

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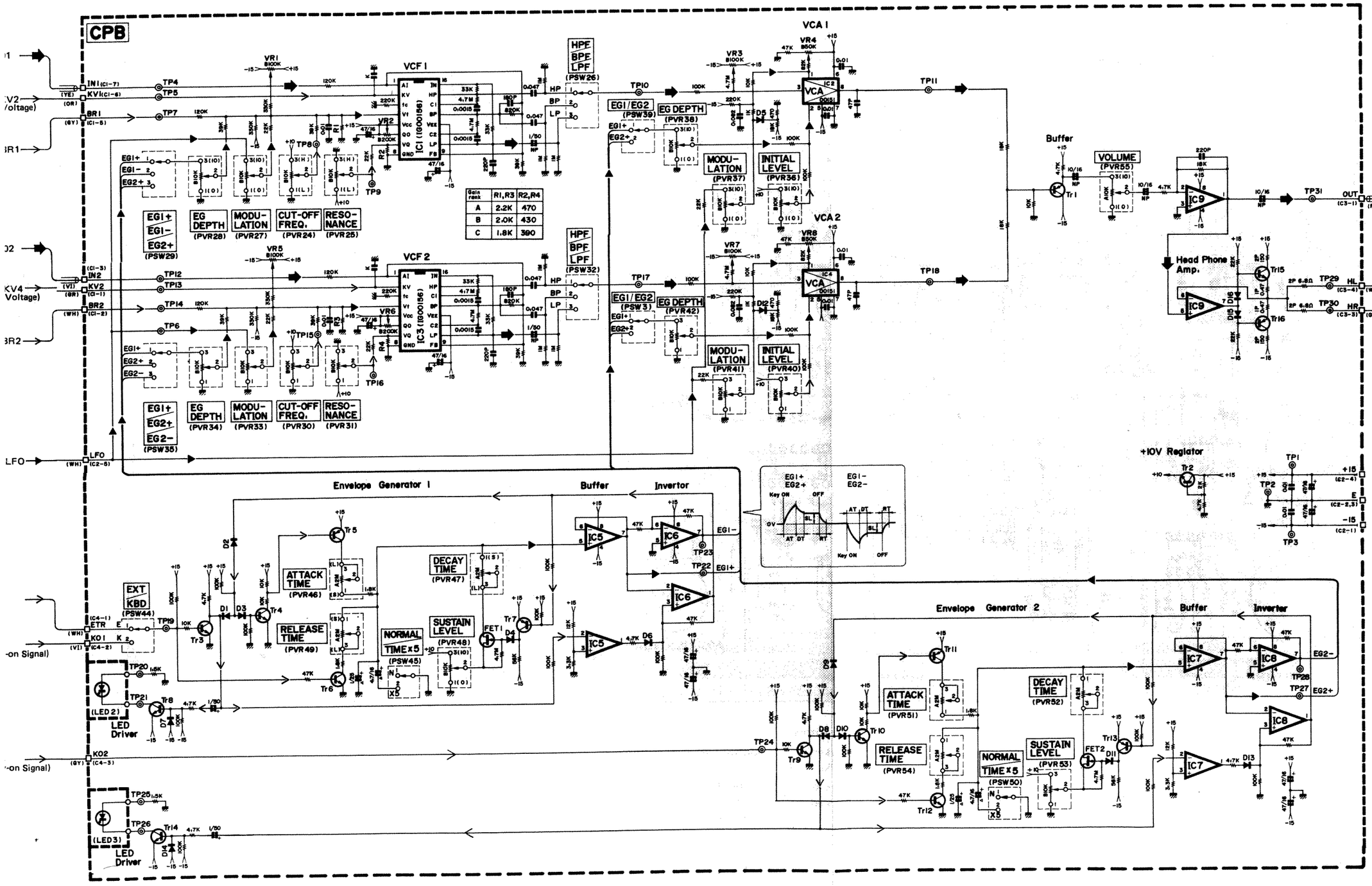
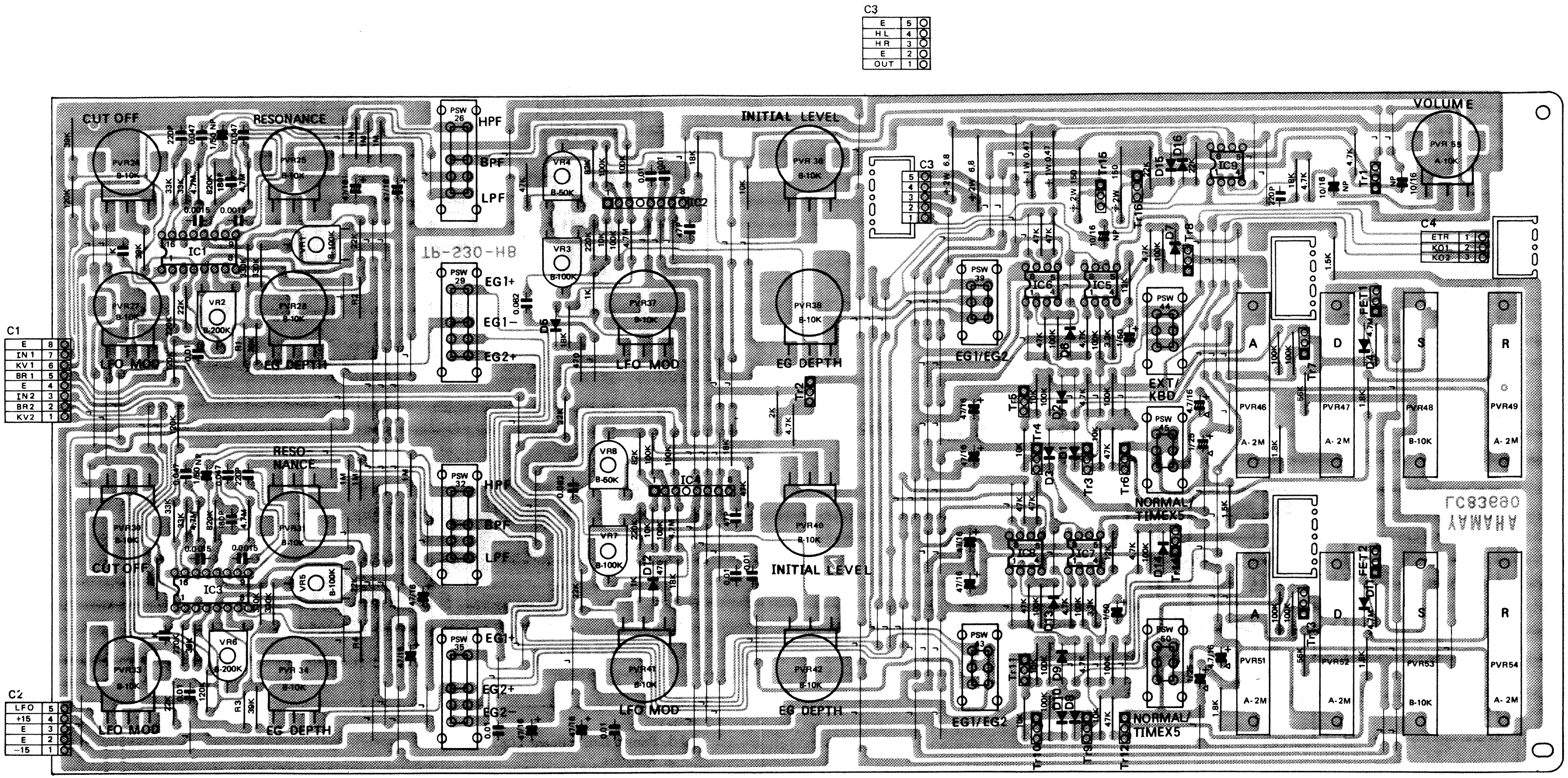
CPB Circuit Diagram





Note)

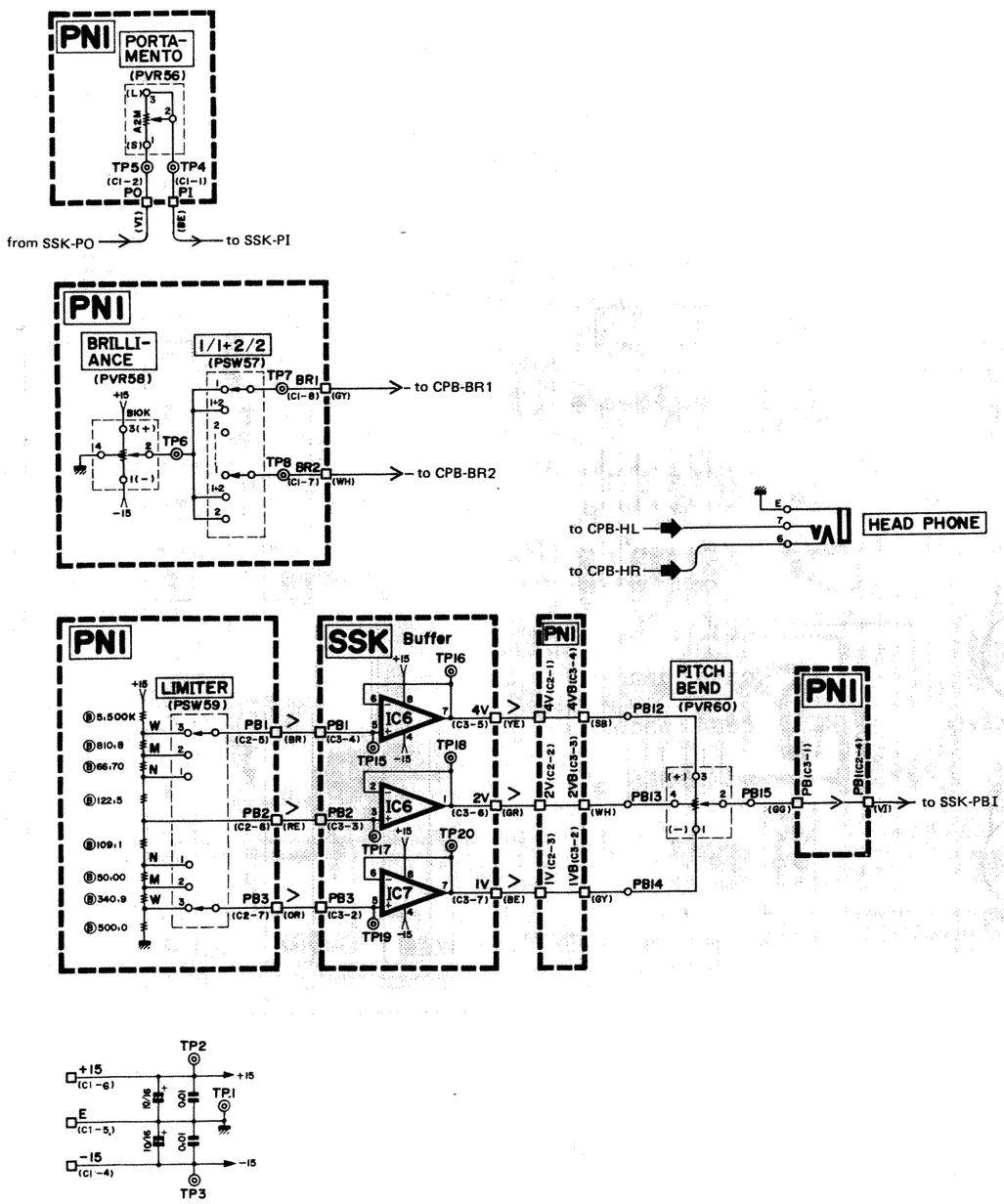
1. Printed Circuit Board LC83690 [2]
2. IC 1, 3 : IG00156
   IC 2, 4 : IG00151
   IC 5 ~ 9 : JNM4558DN
3. Transistors
   Tr 1, 5, 7, 11 13 : 2SA1015 (Y)
   Tr 2, 3, 4, 6
   8 ~ 10, 12, 14 : 2SC1815 (Y)
   Tr 15 : 2SC509 (Y)
   Tr 16 : 2SA509 (Y)
4. Diodes
   D 1 ~ D 16 : 1S1555
5. FET
   FET 1, 2 : 2SK30A (Y)
6. Resistors
   Marked ※ : Metal oxide film resistor
   The values of R1 thru R4 depend on the rank of the IC as follows.

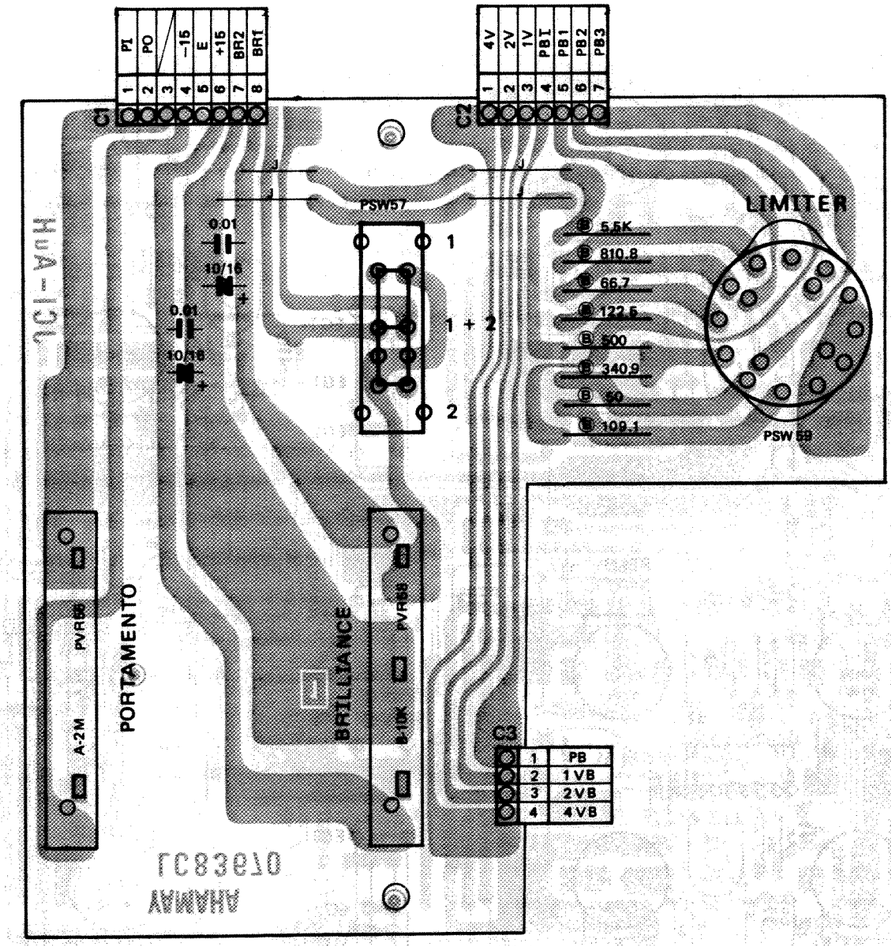
iG00156	R1, R3	R2, R4
A	2.2K	470
B	2.0K	430
C	1.8K	390

7. Capacitors
   Marked "マ" : Mylar capacitor
   Marked △ : Tantalum capacitor
   Marked K : 1,000pF ceramic capacitor
   No mark : Ceramic capacitor

KEP-NA80435-8△

PN1 Circuit Diagram, Circuit Board





Note)

1. Printed Circuit Board LC83670 [1]
2. Resistors
   Marked ® : 0.1% metal film